

CONFIGURABLE, FAST, 32-BIT CRC GENERATOR FOR 1-BYTE TO 16-BYTES  
VARIABLE WIDTH INPUT DATA

ABSTRACT

A method and circuit for performing CRC calculations permits variable width data input. Preferably, multiple CRC calculations are performed in parallel, each CRC calculation involving a different number of data bits from the data word and terminating within one clock cycle. The CRC polynomial is preferably incorporated into the hardware for each CRC calculation.